

AxN Size 4

AxN 70.140.4



Technical specifications ⁽¹⁾	Symbol	AxN 70.140.4	Units
Power Supply Voltage	V_{in}	150 ~ 500	Vac 3 phase
		0 ~ 800	Vdc
Auxiliary supply voltage	V_{aux}	24V \pm 15% / 6A	Vdc
Output frequency	f	0 ~ 1200	Hz
Current output, S1 ⁽²⁾	I_n	70	Arms
Peak current ⁽²⁾	I_p	140	Arms
Power Losses total ⁽³⁾	P_t	870	W
Maximum output voltage	V_{out}	$V_{in} \times 0.95$	Vac
PWM frequency ⁽⁴⁾	f_{pwm}	4 / 8 / 16	kHz
Efficiency at nominal power ⁽²⁾	--	98.1	%
Input form factor (Full load)	--	0.9	Vac 3 phase
Maximum braking current	--	100% of I_p (peak current)	--
Cooling	--	2 PWM fan 80x80x38	--
Flow rate	--	110x2	m ³ / hour
Dimensions (HxDxW)	--	488x249x200	mm

⁽¹⁾ Test performed with full option control card and firmware 1.8.197

⁽²⁾ $V_{in} = 380$ Vac, $V_{out} = V_{in} \times 0.95$, $T_{amb} = 40^\circ\text{C}$, Comm.Freq.8kHz

⁽³⁾ $V_{in} = 380$ Vac, $I_{out} = I_n$, $T_{amb} = 40^\circ\text{C}$, Comm.Freq.8kHz, Including input rectifier losses

⁽⁴⁾ PWM frequency will automatically decrease at Zero speed, in order to keep Nominal current output

Motor Feedback Options

Main Encoder (500kHz)	Sincos encoder 5 channels (2 absolute analog tracks/2 incremental analog tracks/index)
	Incremental encoder (1 Vpp or Different Line Driver)
	Sensorless algorithm (w/o feedback)
	Endat serial encoder 1.0 to 2.2 (default)
	Resolver
Secondary Encoder (500kHz)	Hiperface encoder
	Incremental digital encoder without commutation tracks (500kHz)
	Endat serial encoder

Programmable Input Signals

2 Differential / 4 single ended analog inputs	$\pm 10\text{V}$ (1mV) / Rin = 10k Ω
8 digital inputs	20-30V / Rin = 6.6k Ω to GND
2 Insulated analog inputs (optional)	$\pm 10\text{V}$ (1mV)
8 Insulated digital inputs (optional)	5mA, 24 Vdc max

Programmable Output Signals

2 analog outputs	0-10V (1mV) FS (30mA)
4 digital outputs	PNP open collector 24V (100mA)
1 watch dog relay	2A/30Vdc, 0.25A/250Vac, NO/NC contacts
2 insulated analog output (optional)	$\pm 10\text{V}$ (1mV) FS (30mA)
2 insulated digital output (optional)	On.off switch, 9-28V/2A

Hardware Configuration

Processor speed: 80 MIPS μC + FPGA / 120 MIPS μC + FPGA Extreme Version (Optional)

Task frequency:

- » Current /drive monitoring: 1 MHz
- » Position/speed loop: 8 kHz
- » PLC fast task: 8 kHz
- » PLC slow task: 15.625 Hz to 1 kHz user-programmable

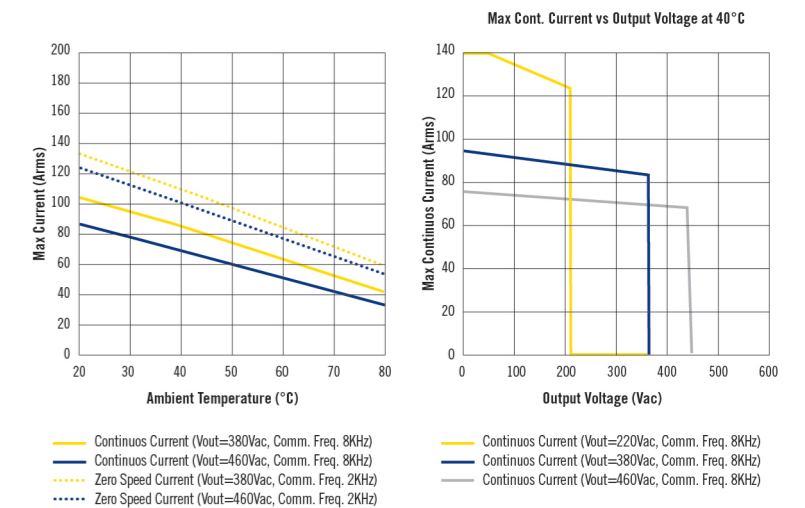
Position loop mode available

Target position register: 32 or 64 bits

Full digital control Id/Iq, updated 16 kHz

Drive Operational Area of AxN Size 4

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Overall Dimensions

